FIG.1

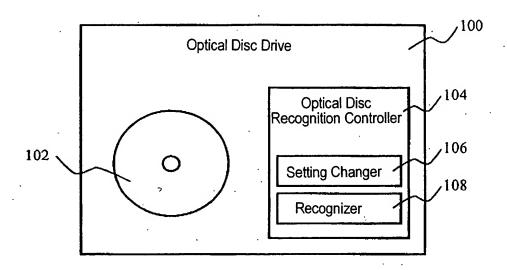


FIG.2

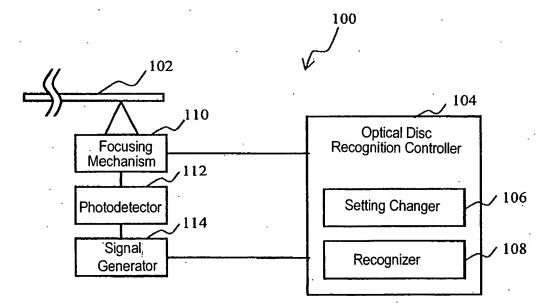


FIG.3

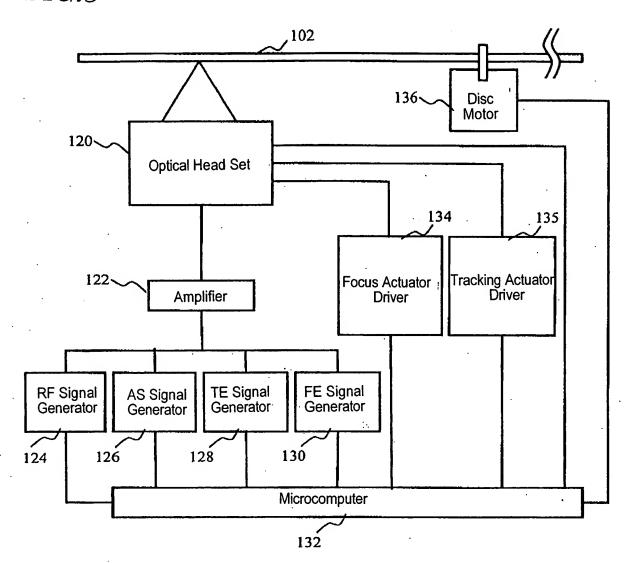


FIG.4

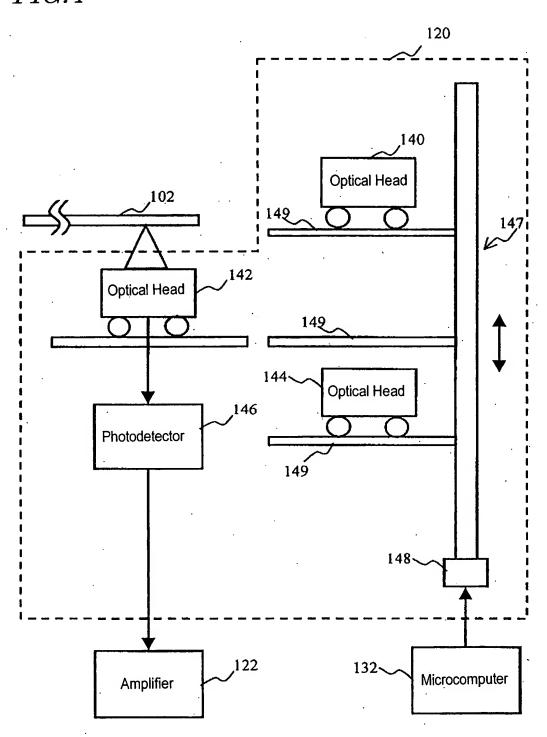


FIG.5

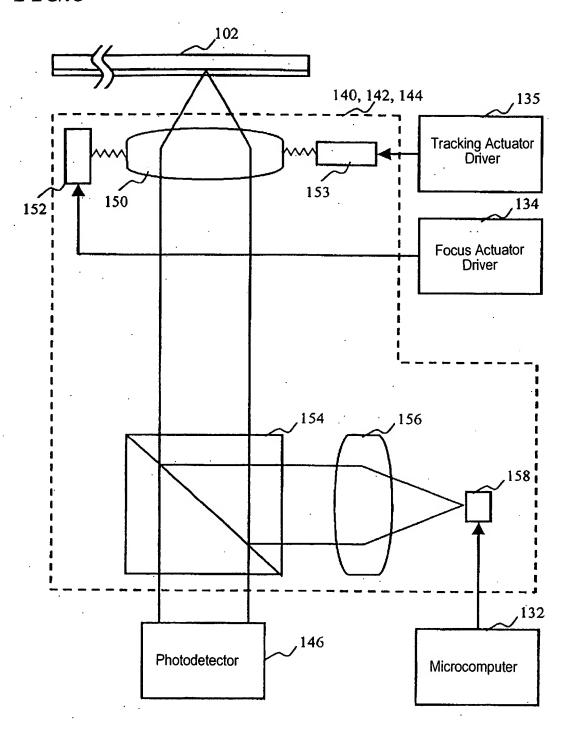


FIG.6

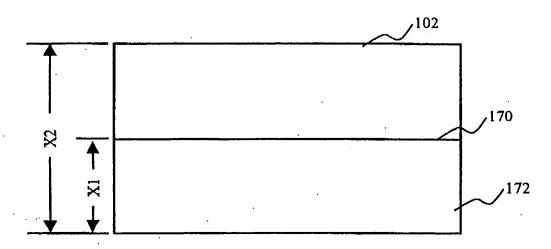
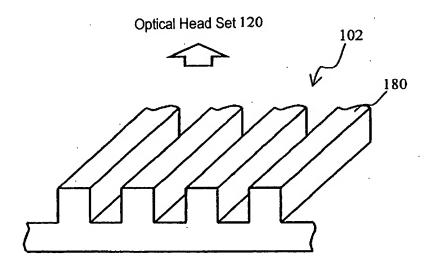


FIG. 7



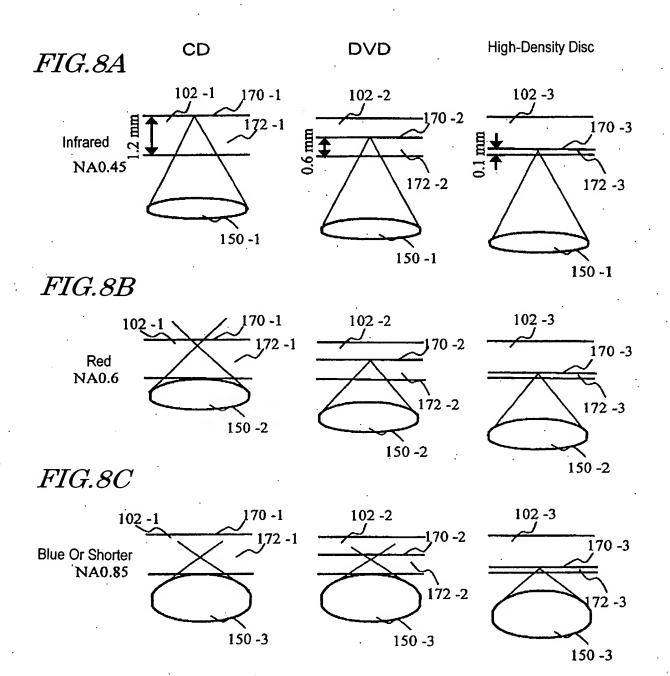


FIG.9

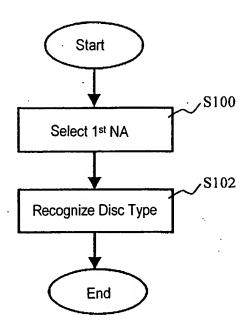
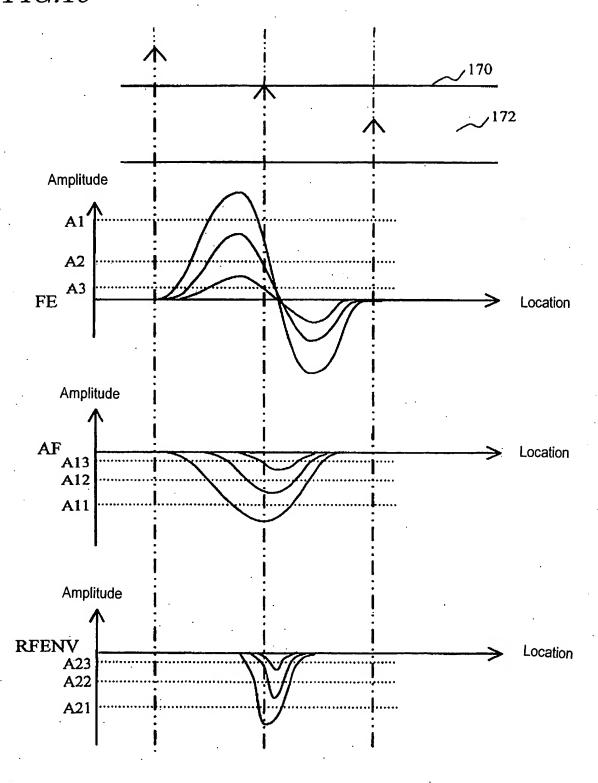


FIG.10



*FIG.11* 

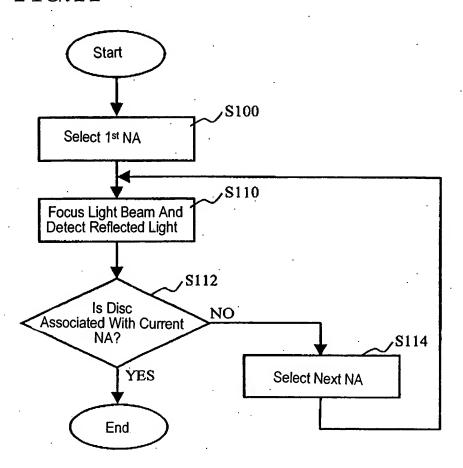


FIG.12

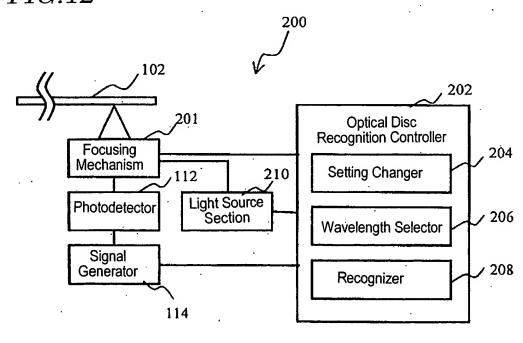


FIG.13

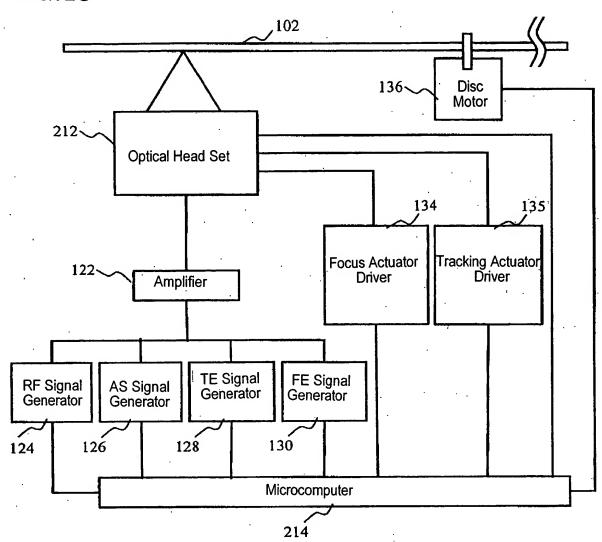
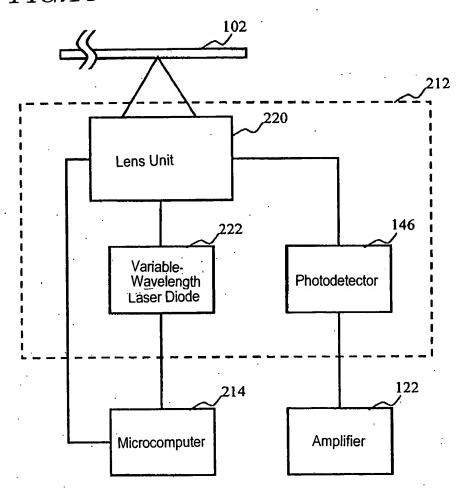
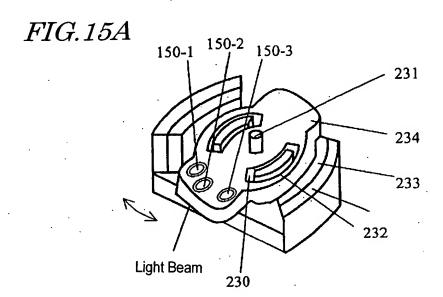


FIG.14





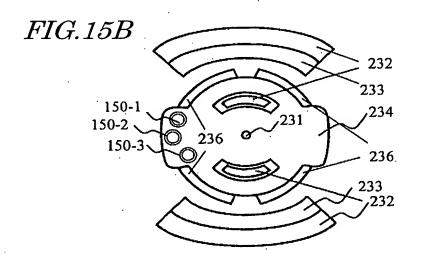


FIG.16

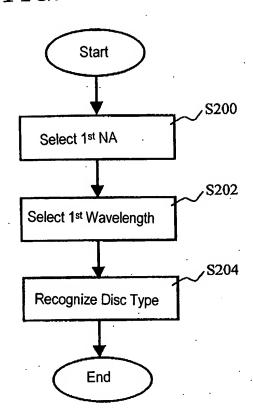


FIG.17

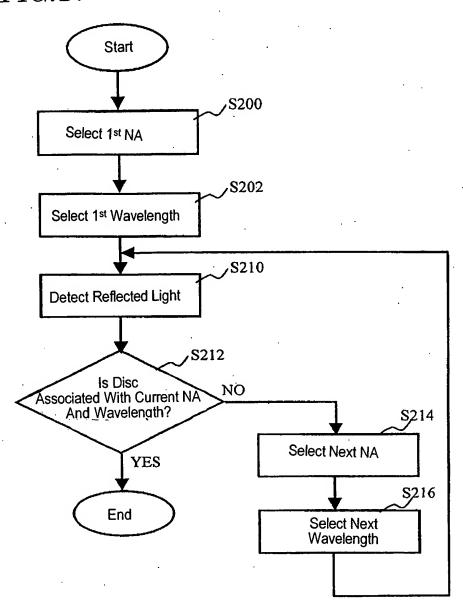


FIG.18

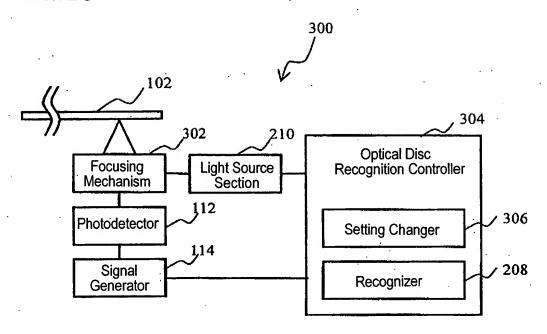
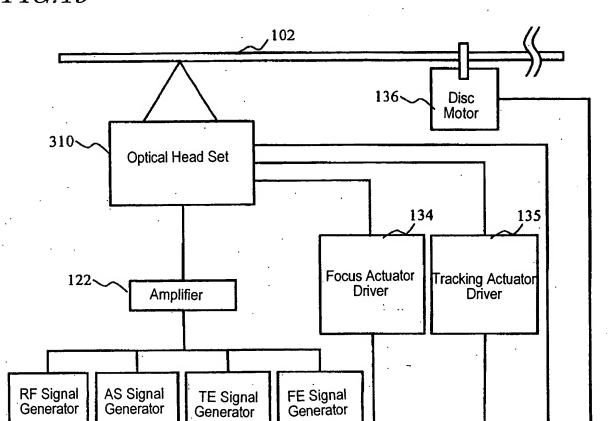
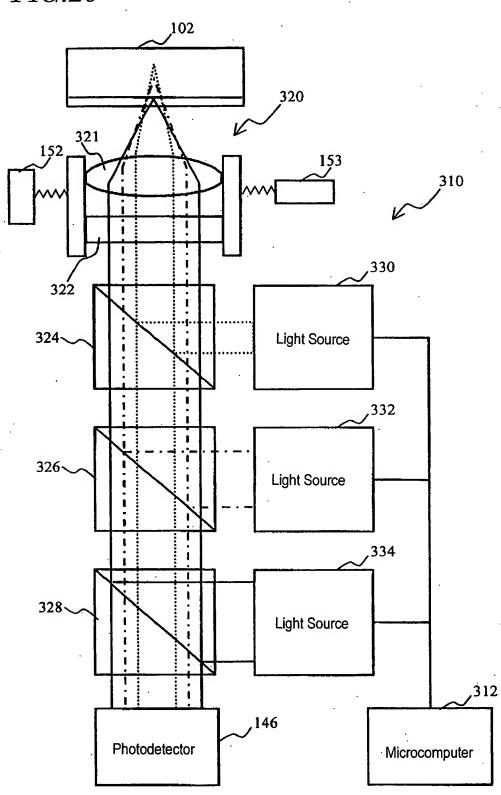


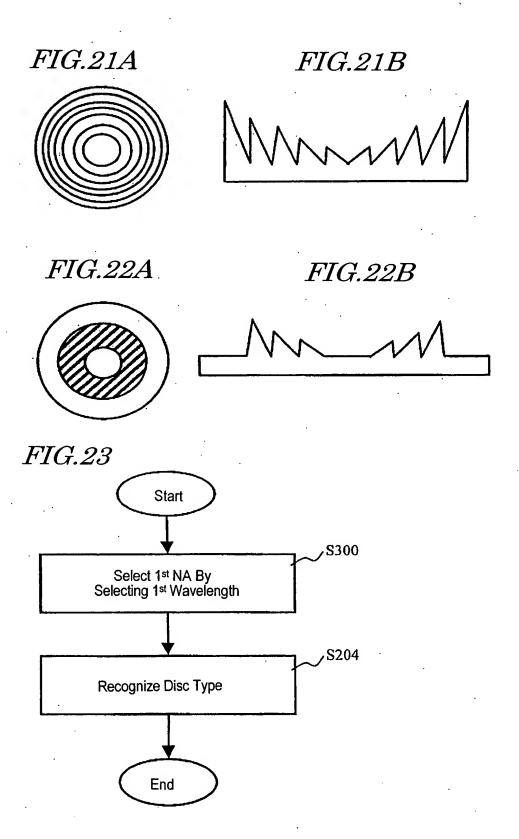
FIG.19



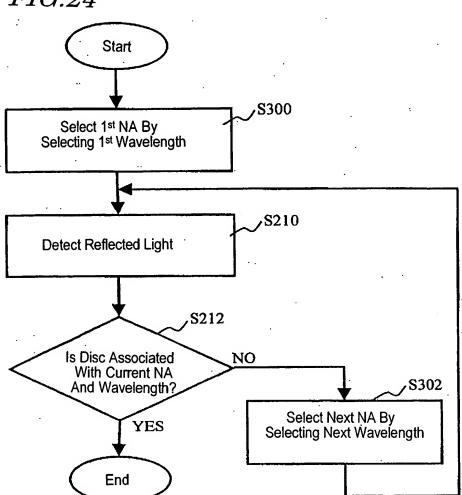
Microcomputer

FIG.20

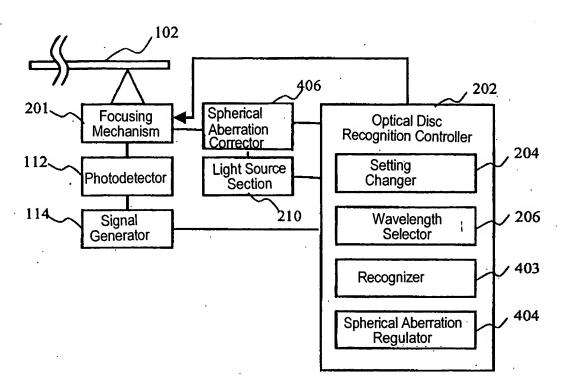


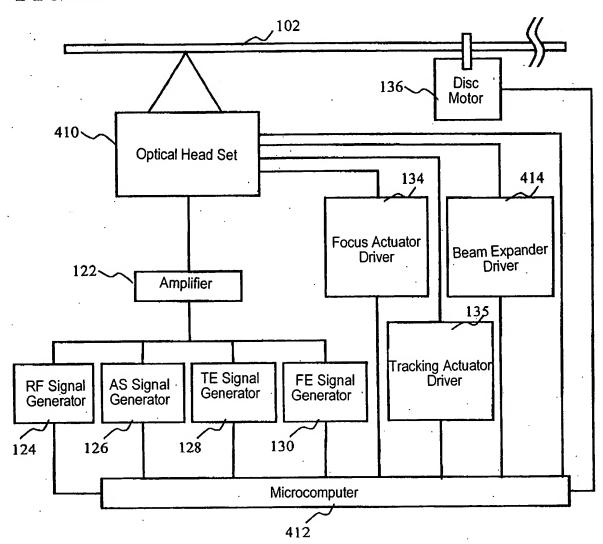


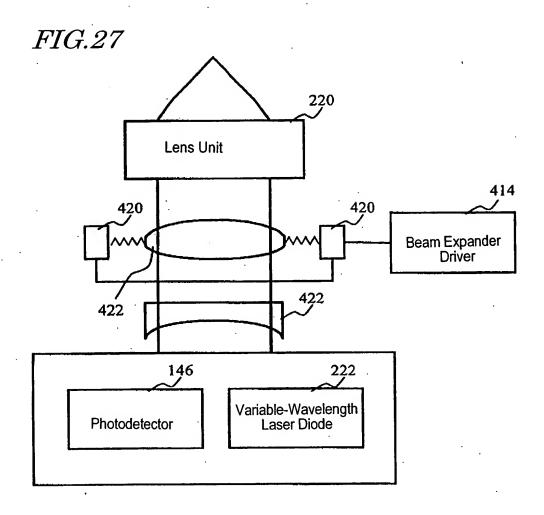




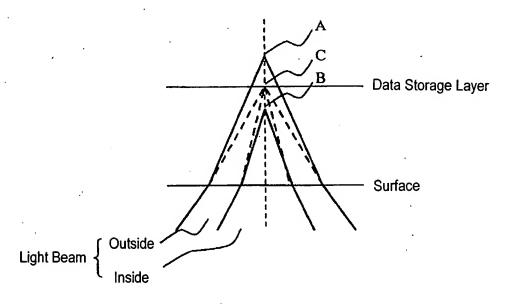
*FIG.25* 

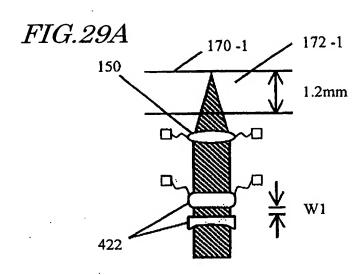






*FIG.28* 





# *FIG.29B*

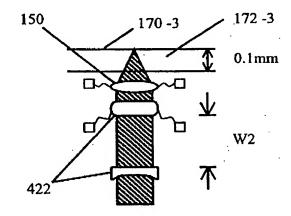


FIG.30

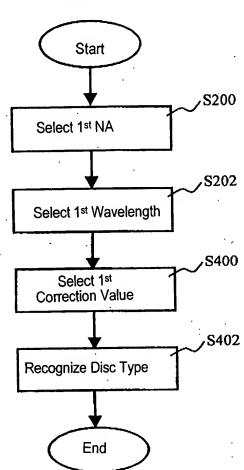


FIG.31

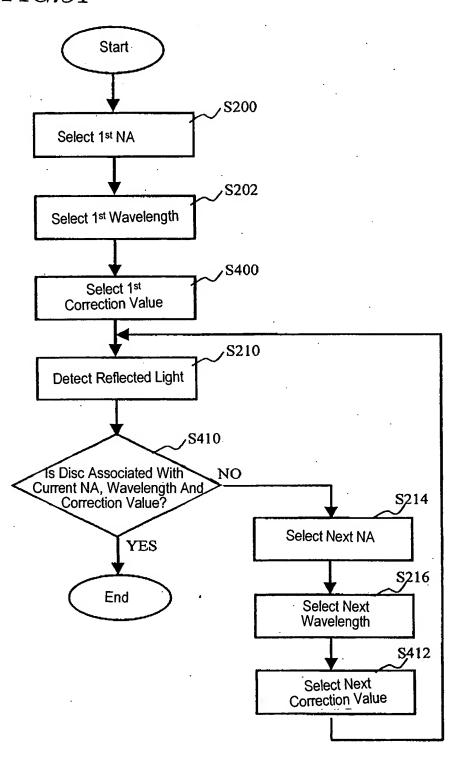


FIG.32

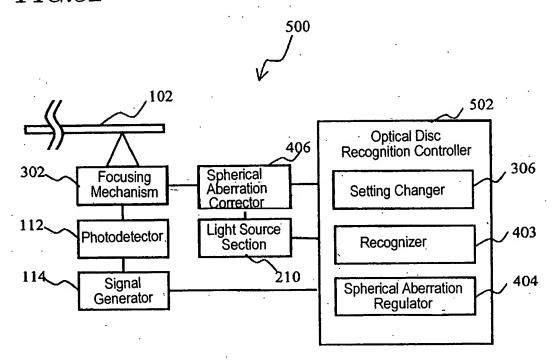


FIG.33

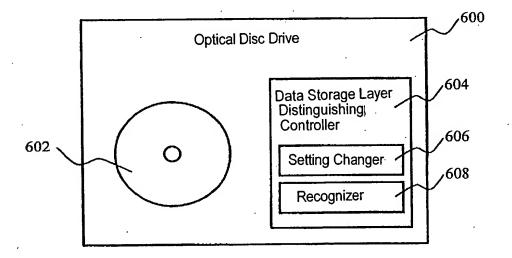


FIG.34

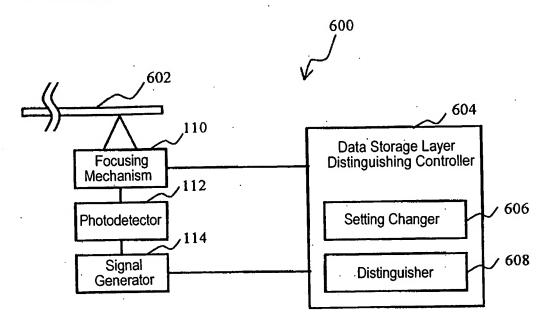


FIG.35

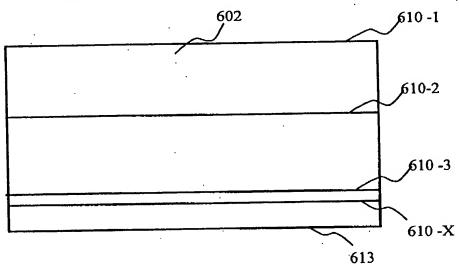


FIG.36

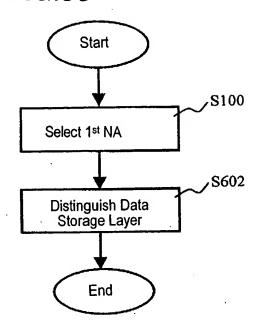
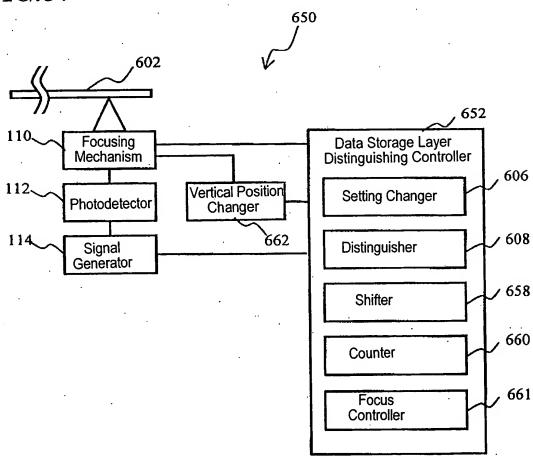


FIG.37



*FIG.38* 

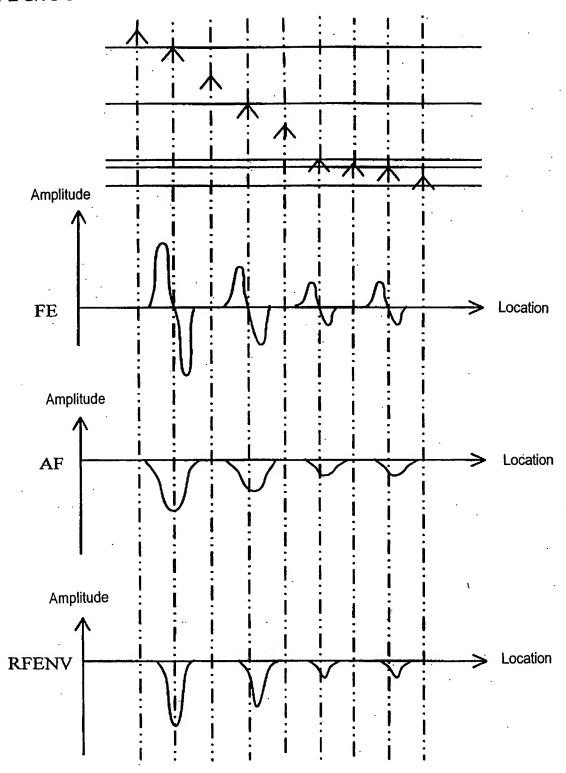


FIG.39

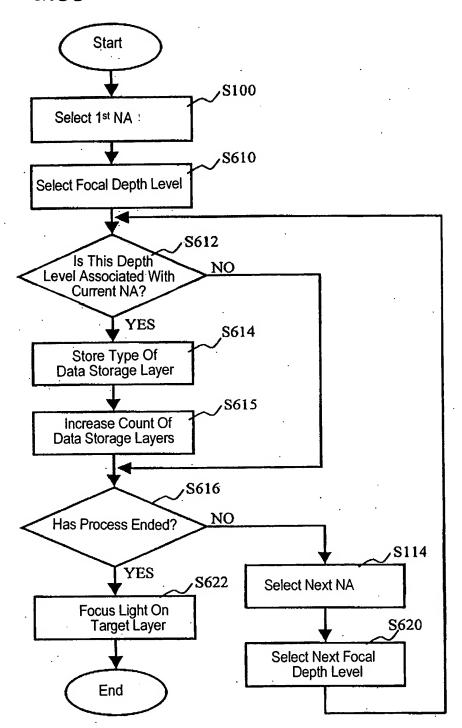


FIG.40

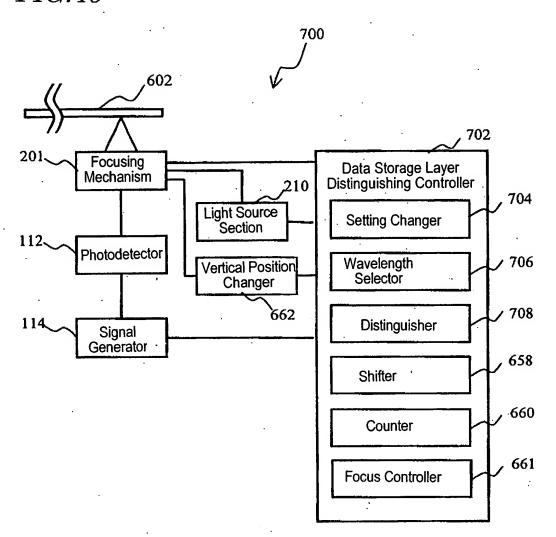


FIG.41

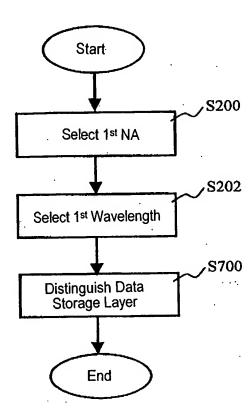


FIG.42

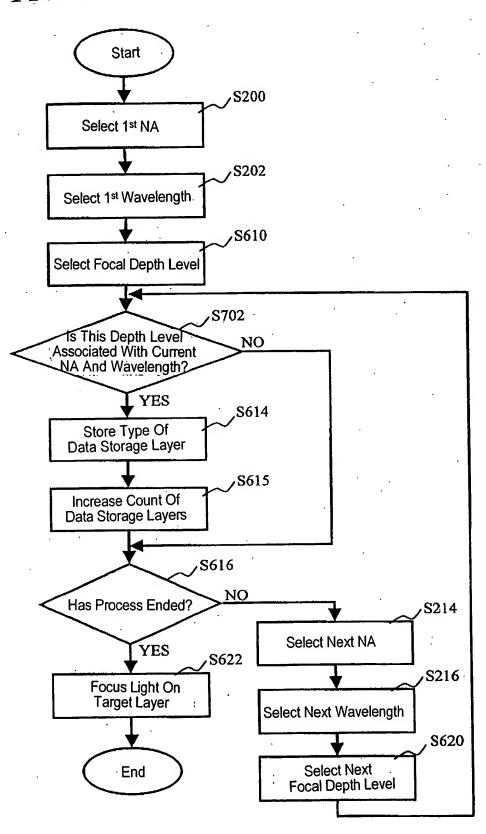
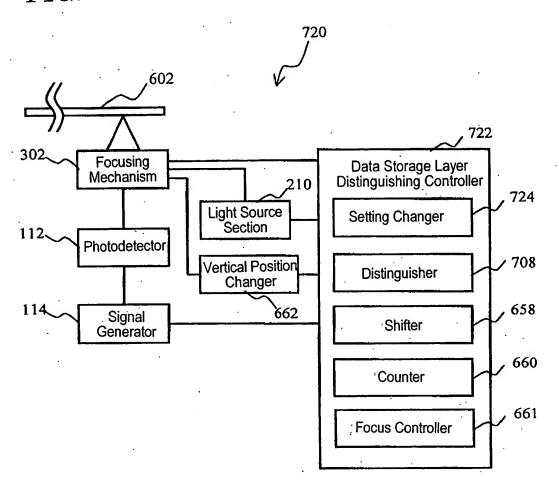


FIG.43



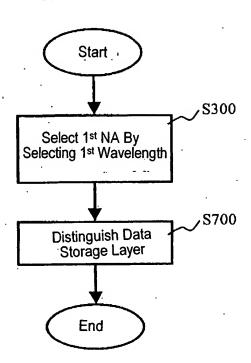


FIG.45

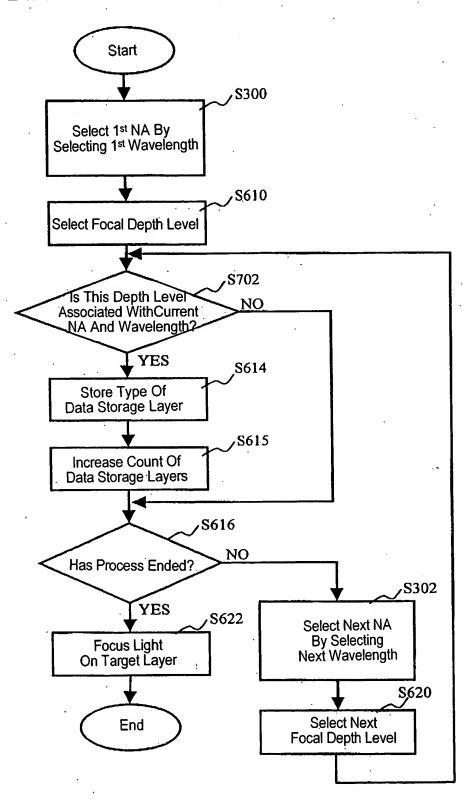


FIG.46

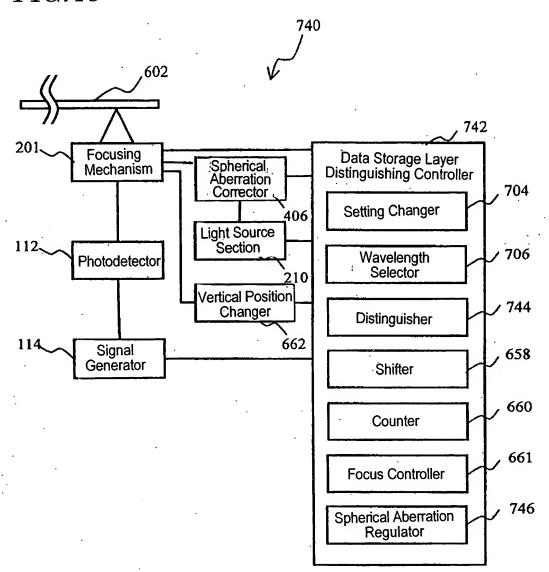


FIG.47

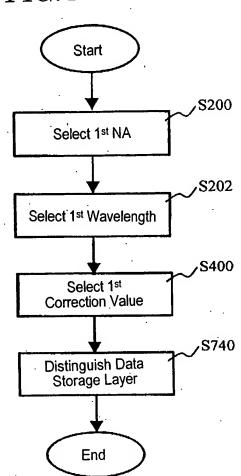


FIG.48

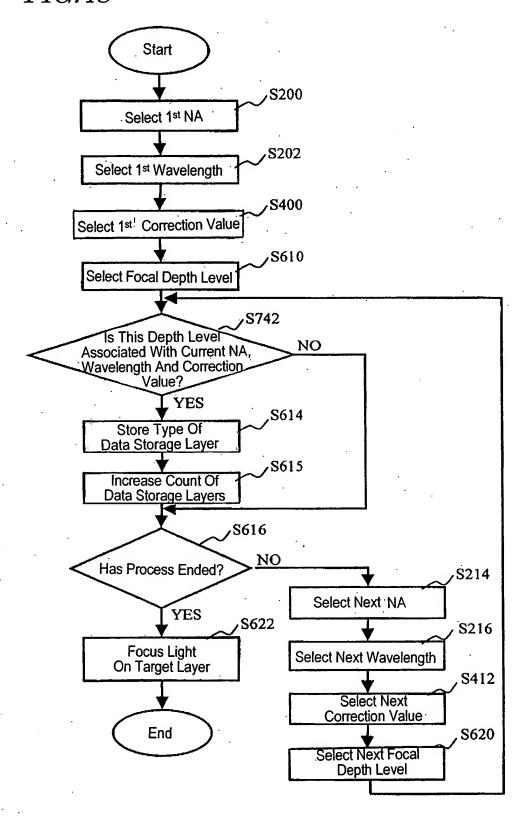
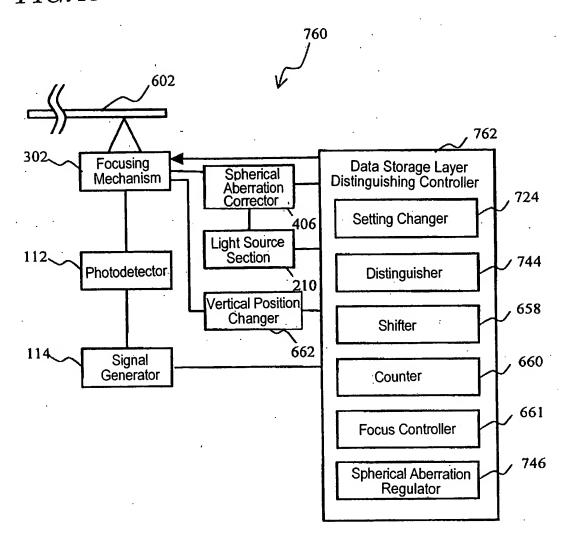


FIG.49



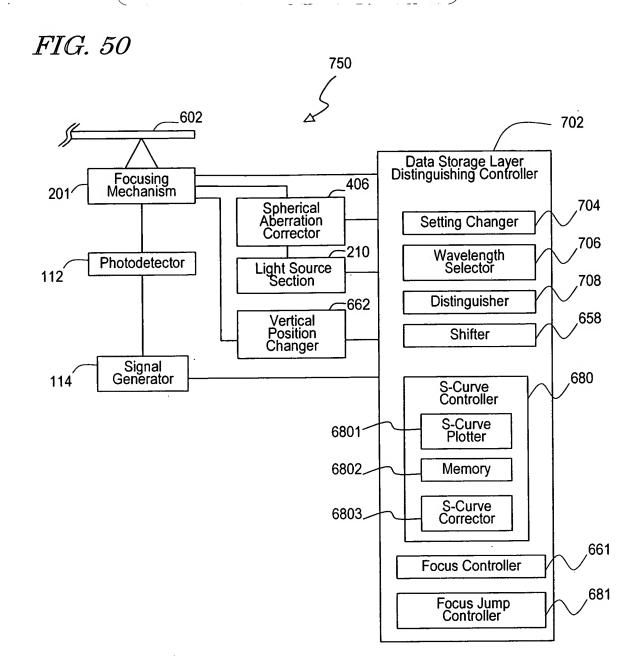


FIG.51

